IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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5201-27500 Atty. Dkt. No. 03-1236

Inventor(s):

Eric Hung Norman Sai

Title:

MEMORY MODULE HAVING

MIRRORED PLACEMENT OF DRAM INTEGRATED CIRCUITS UPON A FOUR LAYER PRINTED CIRCUIT

BOARD

CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. § 1.10

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Date of Deposit: October 23, 2003

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20231.

UTILITY PATENT APPLICATION TRANSMITTAL

(For new non-provisional applications under 37 CFR § 1.53(b))

Enclosed are the following items:

1. The Commissioner is hereby authorized to charge the filing fee to LSI Logic Corporation deposit account no. 12-2252/03-1236. A duplicate of this page is enclosed. The filing fee is calculated as shown below.

Total Claims	20	-20 =	0	x \$18.00 =	\$0.00
Independent Claims	3	-3 =	0	x \$86.00 =	\$0.00
Multiple Dependent Claims				Fee:	
				Basic Fee:	\$770.00
				Total:	\$770.00

- 2. Request & Certification under 35 U.S.C. 122(b)(2)(B)(i).
- 3. Patent application which includes: 15 pg. specification; 6 pg. claims (1-20); 1 pg. abstract.
- 4. Informal drawings of Figs. 1-8 on 6 sheets.
- 5. Newly executed Declaration/Power of Attorney.
- 6. Newly executed Assignment w/cover sheet.
- 7. Return receipt postcard.

lly submitted,

Daffer Reg. No. 34,146

Attorney for Applicants

Conley Rose, P.C. P.O. Box 684908

Austin, TX 78768-4908 Date: October 23, 2003

PTO/SB/35 (11-00)
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Assistant Commissioner for Patents Box Patent Application Washington, DC 20231

Damela Berik

REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)

Atty Docket Number: First Named Inventor:		5201-27500		
		Eric Hung et al.		
Title:	MEMORY MODULE HAVING MIRRORED		_	

MEMORY MODULE HAVING MIRRORED PLACEMENT OF INTEGRATED CIRCUITS UPON A FOUR-LAYER PRINTED CIRCUIT BOARD

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

October 23, 2003

Date

Kevin Ł. Daffer, Reg. No. 34,146

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).

Burden Hour Statement: This collection of information is required by 37 CFR 1.213(a). The information is used by the public to request that an application not be published under 35 U.S.C. 122(b) (and the PTO to process that request). Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 6 minutes to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.